# **S78DL33F**

unit: mm

Low Drop output Voltage Regulator

#### **Descriptions**

• Three Terminal Positive Low Dropout Voltage Regulator

#### **Features**

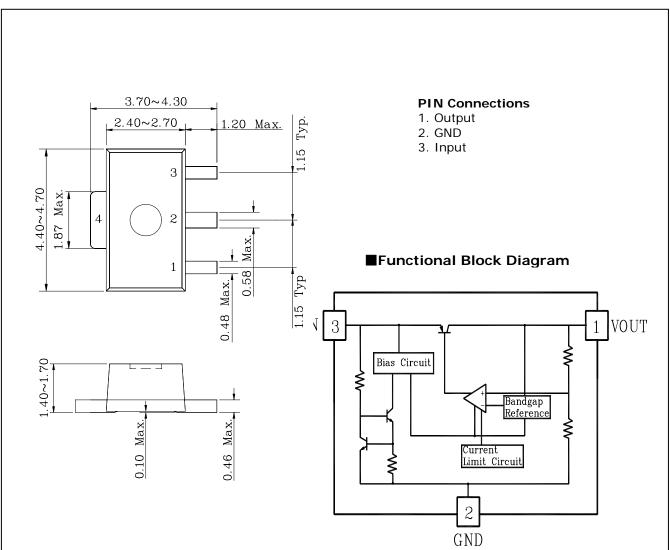
- Low Standby Current Consumption (500 / Typ.)
- Maximum Output Current (180 mA Max.)
- Less I/O voltage Difference (250 mV Max.)

# **Ordering Information**

Type NO.	Marking	Package Code		
S78DL33F	33□□	SOT-89		

□□: Monthly Code, Weekly Code

### **Outline Dimensions**



**Maximum ratings** 

Ta=25°C

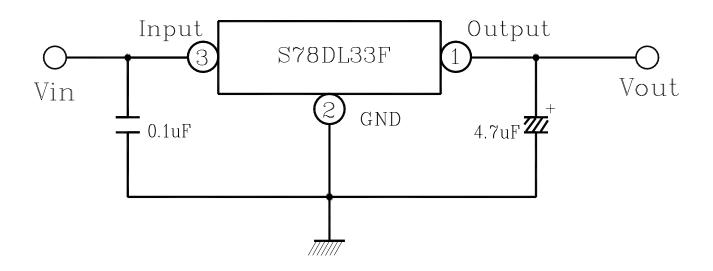
Characteristic	Symbol	Ratings	Unit
Operating Input voltage	V <sub>IN</sub>	16	V
Power Dissipation	$P_D$	500	mW
Operating Temperature Range	T <sub>OPR</sub>	-40~+85	°C
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature Range	T <sub>stg</sub>	-55~150	°C
Lead Temperature Time	T <sub>sol</sub>	260 (10 Sec)	°C

### **Electrical Characteristics**

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Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> =4.3V, I <sub>OUT</sub> =100uA	3.168	3.3	3.432	V
Voltage Regulation	∆ V <sub>OUT</sub> (1)	$V_{IN} = 4.3V \sim 10V$ , $I_{OUT} = 100uA$	-	2	15	mV
Load Regulation	∆ V <sub>OUT</sub> (2)	$V_{IN}$ =4.3V , $I_{OUT}$ =1~100mA	-	7	28	mV
Dropout Voltago	$V_{DROP}$	I <sub>OUT</sub> =50mA	1	110	230	· mV
Dropout Voltage		I <sub>OUT</sub> =100mA	-	150	300	
Ripple Rejection Ratio	RR	f=100Hz, I <sub>OUT</sub> =100uA	-	75	-	dB
	I <sub>GND</sub>	$V_{IN}$ =4.3V , $I_{OUT}$ =100uA		200	400	uA
Ground pin Current		$V_{IN}$ =4.3V , $I_{OUT}$ =50mA		0.9	1.8	mA
		$V_{IN}$ =4.3V , $I_{OUT}$ =100mA		2.1	4	mA

# **■**Test circuit



#### **Electrical Characteristic Curves**

Fig. 1. Vin - Vout

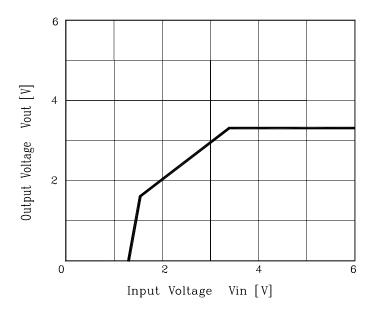


Fig. 3. Pd - Ta

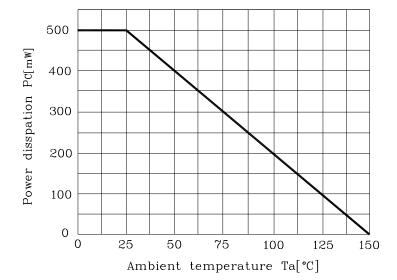


Fig. 2  $|Vout - Vin| - I_{OUT}$ 

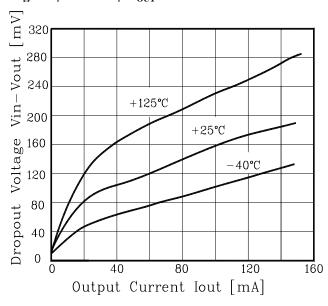
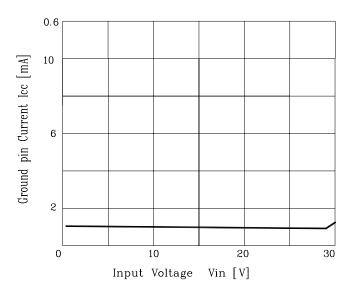


Fig. 4. Input voltage – Ground pin Current



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